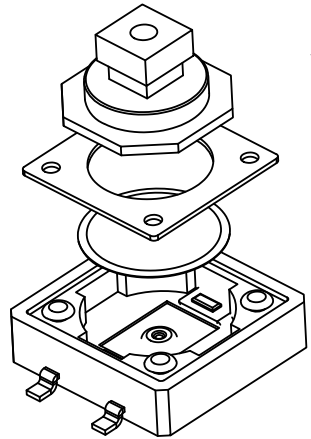




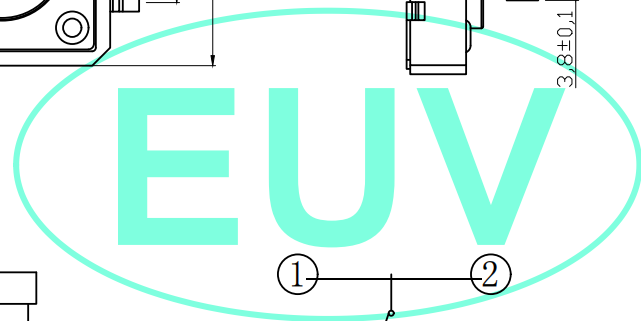
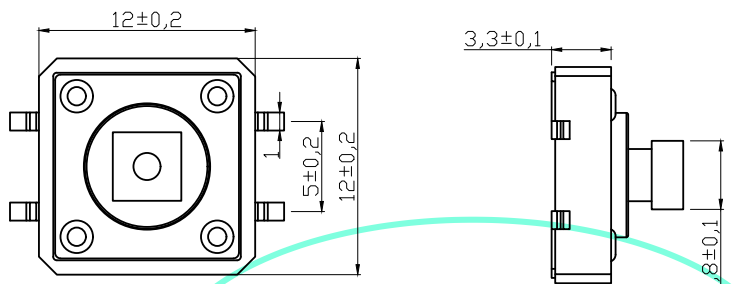
REV. LTR.	DESCRIPTION	APPROVAL	DATE
A2	Update drawing	W. Q. LI	2025. 01. 01



Outside drawing

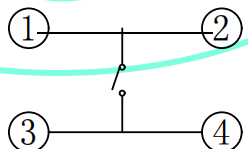
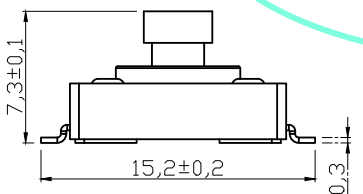


E
D
C
B
A

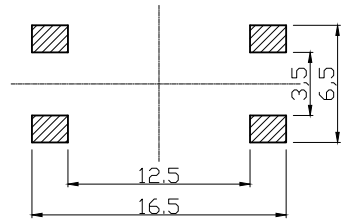
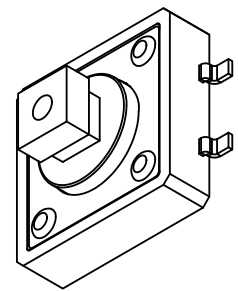


Ordering information:
 EAS-135-□ □ □ □ □
 (1) Series No.
 (2) Optional Force: Reference No. 1
 (3) Cover:
 A=SPCC-SD, B=Brass
 (4) Contact:
 E=SUS301 (Ag)
 D=Phosphor bronze (Ag)
 (5) Knob Color:
 B=Black, R=Red, W=White, Y=Yellow
 (6) Packing mode:
 R=Reel Packing
 B=Bag Packong

NO. 1 (UNIT: gF)	
PART NO	VIGOR
10	100±30
16	160±50
25	250±50
35	350±50



Circuit diagram



Recommended P.C.B Layout
 General Tolerance ±0.05

Specification:
 1. Current Range: DC 12V 50mA.
 2. Contact resistance: ≤50mΩ.
 Insulation Resistance: ≥100MΩ.
 3. Travel: 0.35±0.1mm
 4. Operating Force: No. 1.
 5. Operating Life: 100,000cycles min.
 6. Operation temperature range: -20℃ ~ 70℃.
 7. Preservative temperature range: -20℃ ~ 85℃.

E	Knob/按钮	1	PPA	Black
D	Cover/盖板	1	Brass	Plating Nickel
C	Contact/弹片	1	F. Ag/SUS301	Silver-coated
B	Base/基座	1	PPA	Black
A	Terminal/引脚	4	Brass	Plating Silver
ITEM	NAME	Q' TY	MATERIAC	FINISH

http://www.euvchn.com		EUV ELECTRONICS CO., LTD 兆進電子有限公司			
30~Above ±0.30	ANGLE: $\angle \pm 1^\circ$	TITLE	SMD Tact Seitchs		
10~30 ±0.20	SCALE: 1:1	PART NO.	EAS-135-*BE-B-R		
5~10 ±0.15	SHEET: 1 OF 1	PROJECT	APPROVAL	CHECK	DATE
0~5 ±0.10	PROJECT	APPROVAL	CHECK	DRAWN	DATE
TOLERANCES ARE (UNIT: mm)	SIZE: A4	W. Q. LI	X. H. LIN	K. J. ZENG	2025. 01. 01